



The diagram illustrates a cross-section of a 40T CMOS process. It shows a p-well (labeled p+) and an n-well (labeled n+). A floating deep-well is also present, labeled "Floating Deep-Body". The diagram includes labels for the p+, n+, and p+ regions, as well as the "D-body" and "Floating Deep-Body" structures. The 40T label is shown at the bottom right.

FIG. 2B